



\*\*\* selected transistor

\*\* resistor values to be adapted to the noise level

Note: the noise submodule and the Sample&Hold submodule

must have separate power supply rails in order to avoid interferences

+Vsh & -Vsh power rails for sample & hold submodule

+Vn & -Vn power rails for noise submodule

**NOISE & RANDOM GENERATOR**

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