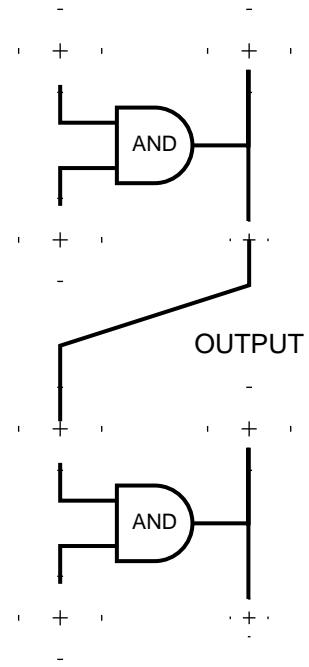


LOGICAL

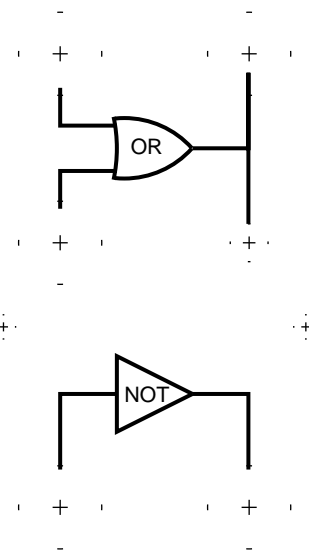
INPUTS

OUTPUT



INPUTS

OUTPUT



Yusynth